PROJECT: SRMS (-5 MCTU INSTALLED)
ASS'Y MOMERICATURE: MCTU

SYSTEM: ELECTRICAL SUBSYSTEM

			A:	SS'Y NOMENCLATURE: MC	ASS'Y P/N: 51155F160-5 SHEET:
FHEA REF.	FMEA REV.	NAME, QTY, & DRAWING REF. DESIGNATION	FATLURE MODE AND CAUSE	FAILURE EFFECT ON END ITEM	HDWR / FUNC. RATIONALE FOR ACCEPTANCE 2/1R CRITICALITY SCREENS: A-PASS, B-PASS, C-PASS
2095	0	FAILURE DETECTOR GTY. 1. SCHEMATIC B12797	MODE: MAHUAL BRAKE DRIVE CIRCUIT FALLS TO "BRAKES OFF" CAUSE(S): 1) FIRST BRAKE DRIVE FET OR CIRCUIT FALLS SHORTED.	AUTO BRAKES WILL BE APPLIED DUE TO HAW MCIU WATCH DOG TIMER BITE VERIFICATION TEST ONE SECOND AFFER D&C PAMEL BRAKES SWITCH IS TOGGLED FROM OFF TO ON. BRAKE TRUTH TABLE WILL FAIL FOR OME SECOND AFTER BRAKE SWITCH THANSITION. ALL OPERATIONAL MODES ARE STILL AVAILABLE. MORST CASE LOSS OF MANUAL BRAKES. UNEKPECTED MOTION. AUTOBRAKES (FOR SAFING THE SYSTEM)	DESIGN FEATURES THE BRAKE DRIVER IS IMPLEMENTED USING FET POWER TRANSISTORS, CONNECTED IN A SERIES REDUNDANT CONFIGURATION. THE CIRCUIT EMPLOYS CONTINUOUS TESTING TO VERIFY THE INTEGRITY OF THE BRAKE DRIVE CIRCUIT. INDUCTORS ARE DESIGNED SPECIFICALLY FOR THE APPLICATION. THE DESIGN CRITERIA, INCLUDING CHOICE OF MATERIALS AND TEST REQUIREMENTS ARE IN ACCORDANCE WITH MIL-1-27. WORST CASE STRESS LEVELS DO NOT EXCEED THOSE ALLOWED BY SPAR-RMS-PA.003. ALL RESISTORS AND CAPACITORS USED IN THE DESIGN ARE SELECTED FROM ESTABLISHED RELIABILITY (ER) TYPES. LIFE EXPECTANCY IS INCLEASED BY EMSURISK THAT ALL ALLOWABLE STRESS LEVELS ARE DERATED IN ACCORDANCE WITH SPAR-RMS-PA.003. ALL CERAMIC AND ELECTROLYTIC CAPACITORS ARE ROUTIWELY SUBJECTED TO RADIOGRAPHIC INSPECTION. DISCRETE SEMICONDUCTOR DEVICES SPECIFIED TO AT LEAST THE TX LEVEL OF MIL-S-1950D. ALL DEVICES ARE SUBJECTED TO RE-SCREENING BY AN IMPEPENDANT TEST HOUSE. SAMPLES OF ALL PROCURED (015/Date codes are subjected to Destructive Physical AMALYSIS (DPA) TO VERTIFY THE INTEGRITY OF THE MANUFACTURING PROCESSES. DEVICE STRESS LEVELS ARE, DERATED IN ACCORDANCE WITH SPAR-RMS-PA.003 AND VERTIFIED BY DESIGN REVIEW. THE DEC BRAKE WITCH CONTROLS THE OPERATION OF THE BRAKE DRIVER THROUGH AN OPTO-ISOLATOR WHICH ACIS AS A SOLID-STATE RELAY. OPTO-ISOLATORS (DIODE AND TRANSISTOR) MEET THE SAME QUALITY AND APPLICATION CRITERIA THAT HAVE BEEN APPLIED TO DISCRETE SEMICOMOUCTORS.
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EPARED BY:	l : <u>M</u> F	wc	SUPERCEDING DATE	: NONE	DATE: 11 JUL 91 CIL 85V C

DATE: 11 JUL 91

CIL REV: 0

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PF AS	ROJECT: <u>SRMS (-5 MC</u> SS'Y NOM ENCLATURE: M	JU INSTALLED) CIU	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: \$1155F160-5	SHEET:	
4ODE	FAILURE EFFECT	HDWR / FUNC. 2/1R	RATIONALE FOR ACCEPTANCE		
	END ITEM	CRITICALITY	SCREENS: A-PASS, B-PASS, C-PASS		
					

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	FMEA REF.	FMEA REV.	HAME GTY, & DRAWING REF.	FAILURE MODE AND	FAILURE EFFECT ON	HDWR / FUNC. 2/1R	RATIONALE FOR ACCEPTANCE
-			DESIGNATION	CAUSE	END ITEM	CRITICALITY	SCREENS: A-PASS, B-PASS, C-PASS
	2095	0	DESIGNATION FAILURE DETECTOR GTY. 1. SCHEMATIC B12797	CAUSE MODE: MANUAL BRAKE DRIVE CIRCUIT FAILS TO "BRAKES OFF" CAUSE(S): 1) FIRST BRAKE DRIVE FET OR CIRCUIT FAILS SHORTED.	AUTO BRAKES VILL BE APPLIED DUE TO H/W NCIU WATCH DOG TIMER BITE VERIFICATION TEST ONE SECOND AFTER D&C PAMEL BRAKES SWITCH 1S TOGGLED FROM OFF TO ON. BRAKE TUITH TABLE WILL FAIL FOR OME SECOND AFTER BRAKE SWITCH TRANSITION. ALL OPERATIONAL MODES ARE STILL AVAILABLE. WORST CASE LOSS OF MANUAL BRAKES. UNEXPECTED MOTION. AUTOBRAKES (FOR SAFING THE SYSTEM)	CRITICALITY ACCEPTANCE TE THE MCIU IS SIENVIRONMENTAL O VIBRATION: O THERMAL: QUALIFICATION THE MCIU IS SIENVIRONMENTS: O VIBRATION: O SHOCK: O THERMAL: O MUMIDITY: O EMC: G LIFE:	JUDIECTED TO THE FOLLOWING ACCEPTANCE TESTING AS AN LRU. LEVEL AND DURATION - REFERENCE TABLE 3.2 +40 DEGREES C TO -16 DEGREES C (2 CYCLES) TESTS JUDIECTED TO THE FOLLOWING LRU QUALIFICATION LEVEL AND DURATION - REFERENCE TABLE 3.2 BY SIMILARITY TO -3 MCIU +51 DEGREES C TO -27 DEGREES C (10 CYCLES) BY SIMILARITY TO -3 MCIU MIL-STD-461 AS MODIFIED BY SL-E 0002 (TESTS CEOT, CEO3, CSO1, CSO2, CSO6, REO2 (M/B), RSO1, RSO2 630 OPERATING HOURS 1000 POWER OM/OFF CYCLES
PRE	PARED BY:	ME	Wu	SUPERCEDING DATE:	HOME		DATE: 11 JUL 91 CIL REV: 0

S040237A ATTACHMENT -PAGE 368 OF 471

ATTACHMENT -

				SS'Y NOMENCLATURE: H	, IU	ASS'Y P/	N: 51155F160-5	SHEET: _	4
MEA EF.	FMEA REV.	NAME QTY & DRAWING RÉF. DESIGNATION	FAILURE MODE AND CAUSE	FAZLURE EFFECT ON END ITEM	HDWR / FUNC. 2/1R CRITICALITY	RATIONALE FOR SCREENS: A-PASS	ACCEPTANCE , B-PASS, C-PASS		<u> </u>
2095	0	FAILURE DETECTOR QTY. 1. SCHEMATIC 812797	MODE: MANUAL BRAKE DRIVE CIRCUIT FAILS TO "BRAKES OFF" CAUSE(S): 1) FIRST BRAKE DRIVE FET OR CIRCUIT FAILS SHORTED.	AUTO BRAKES MILL BE APPLIED DUE 10 H/M MCIU MATCH DOG TIMER BITE VERIFICATION TEST ONE SECOND AFTER DEC PANEL BRAKES SWITCH IS TOGGLED FROM OFF TO ON. BRAKE TRUTH TABLE WILL FALL FOR OME SECOND AFTER BRAKE SWITCH TRANSITION. ALL OPERATIONAL HODES ARE STILL AVAILABLE. MORST CASE LOSS OF MANUAL BRAKES. UNEXPECTED MOTION. AUTOBRAKES UNEXPECTED MOTION. AUTOBRAKES REDUNDANT PATHS REMAINING AUTOBRAKES (FOR SAFING THE SYSTEM)	AND THE GO FORMAL TES ACCEPTANCE	SURANCE IN COMJUNCTION Y, CONFIGURATION CONTR YENGENT REPRESENTATIVE TIME (ACCEPTANCE OR OU TESTING (ATP) INCLUDE STING (SPAR/GOVERNMENT	WITH ENGINEERING, OL, SUPPLIER AS APPL PRIOR TO THE START ALIFICATION).	AND	
									PAGE 370 OF 471
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DATE: 11 JUL 91 CFL REV

SUPERCEDING DATE: NONE

PREPARED BY:

MFMG

PROJECT: SRMS (-5 MCJU INSTALLED) ASS'Y NOMENCLATURE: MCJU	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/R: 51755F160-5

MERA EEF. REV. REV. RAUMAN PARCE DAVIDE OF CAUSE DAVIDE OF STREET DAVID OF STR		WR FIRT		PROJECT: <u>SRMS (-5 MC)</u> ASS'Y NOM ENCLATURE: <u>RC</u>	TU TWSTACLED)	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: 51155F160-5	SHEET:
PATURE DETECTOR DIT 1, SCHEWATTE STATE OF THE STATE OF TH		NAME, QTY, & DRAWING REF. DESIGNATION	AND	ON	2/1R	RATIONALE FOR ACCEPTANCE	-
	2095 0	FAILURE DETECTOR QTY. 1. SCHEMATIC	MODE: MANUAL BRAKE DRIVE CIRCUIT FAILS TO "BRAKES OFF" CAUSE(S): 1) FIRST BRAKE DRIVE FET OR CIRCUIT FAILS	AUTO BRAKES WILL BE APPLIED DUE TO H/W MCIU WATCH DOG TIMER BITE E VERIFICATION TEST ONE SECOND AFTER DAC PANEL BRAKES SWITCH IS TOGGLED FROM OFF TO ON. BRAKE TRUTH TABLE WILL FAIL FOR ONE SECOND AFTER BRAKE SWITCH TRANSITION. ALL OPERATIONAL MODES ARE STILL AVAILABLE. WORST CASE LOSS OF MANUAL BRAKES. UNEXPECTED MOTION. AUTOBRAKES REDUNDANT PATHS REMATHING AUTOBRAKES (FOR SAFING THE	FAILURE HIST	ORY EEN NO FAILURES ASSOCIATED WITH THIS FAILURE	

SO40237A ATTACHMENT -PAGE 371 OF 471

PROJECT: SRMS (-5 MC1U II ASS'Y NOMENCLATURE: MC1U	(STALLED) ST	YSTEM: <u>ELECTRICAL SUBSYSTEM</u> SS'Y P/N: <u>51155F160-5</u> SHE	EI:
			

2095 O FAILURE DETECTOR GIY. 1. SCHEMATIC B12797 FAILS TO MERCES OFF" CAUSE(S): ONE SECOND TIEST ONE SECOND TIEST ONE SECOND TIEST ONE SECOND THRE BRAKE DRIVE FAILS TO THE BRAKE SWITCH OFF TO ON. CIRCUIT OFF TO ON. CIRCUI	ONE SECOND AFTER MANUAL BRAKES
LOSS OF MANUAL BRAKES. UNEXPECTED MOTION. AUTOGRAKES REDUNDANT PATHS REMAINING AUTOGRAKES (FOR SAFING THE SYSTEM)	BSERVE WHETHER THE ARM IS LANDS. IF IT ISN'T, APPLY BRAKES.

SO40237A ATTACHMENT -PAGE 372 OF 471

RITIC	AL ITE	MS LIST	PAS AS	ROJECF: <u>SRMS (-5 MC</u> BS'Y NOM <mark>enclature: <u>M</u>i</mark>	IU (NSTALLED)	;	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: 51755F160-5	SHEET:	
FHEA REF.	FHEA REV.	HAME GTY & DRAWING REF. DESIGNATION	FATLURE MODE AND CAUSE	FAILURE EFFECT ON END ITEM	HDWR / FUNC. 2/18 CRITICALITY		NALE FOR ACCEPTANCE A-PASS, B-PASS, C-PASS		
2095	0	FAILURE DETECTOR GTY. T. SCHEMATIC B12797	MODE: MANUAL BRAKE DRIVE CIRCUIT FAILS TO "BRAKES OFF" CAUSE(S): 1) FIRST BRAKE DRIVE FET OR CIRCUIT FAILS SHORTED.	AUTO BRAKES WILL BE APPLIED DUE TO HYM MCIU MATCH DOG TIMER BITE VERIFICATION TEST ONE SECOND AFTER DEC PANEL BRAKES SWITCH ISTORMED FROM OFF TO ON. BRAKE TRUTH TABLE WILL FAIL FOR ONE SECOND AFTER BRAKE SWITCH TRANSITION. ALL OPERATIONAL MODES ARE STILL AVAILABLE. WORST CASE LOSS OF MANUAL BRAKES. UNEMPECTED MOTION. AUTOBRAKES REDUNDANT PATHS REMAINING AUTOBRAKES (FOR SAFING THE SYSTEM)	SCREEN FAILURES				
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		EM8 LIST	,	ROJECT: SRMS (-5 MC SS'Y NOMENCLATURE: M	CIU	SYSTEM: ELECTRICAL SUBSYSTEM ASS'Y P/N: 51155F180-5 SHEET:
FHEA REF.	FMEA REV.	HAME GTY & DRAWING REF. DESIGNATION	FAILURE MODE AND CAUSE	FAILURE EFFECT ON END ITEM	HDWR / FUNC. 2/1R CRITICALITY	RATIONALE FOR ACCEPTANCE SCREENS: A-PASS, 8-PASS, C-PASS
2095	0	FAILURE DETECTOR GTY. 1. SCHEMATIC B12797	MODE: MANUAL BRAKE DRIVE CIRCUIT FAILS TO "BRAKES OFF" CAUSE(S): 1) FIRST BRAKE ORIVE FET OR CIRCUIT FAILS SHORTED.	AUTO BRAKES WILL BE APPLIED BUE TO H/M MCIU WATCH DOG TIMER BITE VERIFICATION TEST ONE SECOND AFTER D&C PANEL BRAKES SWITCH IS TOGGLED FROM OFF TO ON, BRAKE TRUTH TABLE WIPL FAIL FOR ONE SECOND AFTER BRAKE SWITCH TRANSITION. ALL OPERATIONAL MODES ARE STILL AVAILABLE. WORST CASE LOSS OF MANUAL BRAKES. UNEXPECTED MOTION. AUTOBRAKES REDUNDANT PATHS REMAINING AUTOBRAKES (FOR SAFING THE SYSTEM)	OMRSD OFFLI APPLY BRAKE BRAKE VOLTA OMRSD ONLIN OPERATE BRA INTERFACE.	
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S040237A ATTACHMENT -PAGE 374 OF 471

SUPERCEDING DATE: NONE PREPARED BY: MFWG RMS/ELEC - 133

DAIE: 11 JUL 91

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